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09/22/2006

STEVENS DAVIS MILLER & MOSHER, LLP
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WASHINGTON, DC 20036

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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
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10/804,138

03/19/2004

Osamu Ichikawa

L8462.04111

6381

TITLE OF INVENTION: SEMICONDUCTOR INTEGRATED CIRCUIT AND TEST METHOD THEREOF

APPLN. TYPE	SMALL ENTITY	ISSUE FEE DUE	PUBLICATION FEE DUE	PREV. PAID ISSUE FEE	TOTAL FEE(S) DUE	DATE DUE
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nonprovisional

NO

\$1400

\$300

\$0

\$1700

12/22/2006

EXAMINER	ART UNIT	CLASS-SUBCLASS
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BRITT, CYNTHIA H

2138

714-710000

1. Change of correspondence address or indication of "Fee Address" (37 CFR 1.363).

☐ Change of correspondence address (or Change of Correspondence Address form PTO/SB/122) attached.

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1 STEVENS, DAVIS, MILLER

2 & MOSHER, LLP

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3. ASSIGNEE NAME AND RESIDENCE DATA TO BE PRINTED ON THE PATENT (print or type)

PLEASE NOTE: Unless an assignee is identified below, no assignee data will appear on the patent. If an assignee is identified below, the document has been filed for recordation as set forth in 37 CFR 3.11. Completion of this form is NOT a substitute for filing an assignment.

(A) NAME OF ASSIGNEE

(B) RESIDENCE: (CITY AND STATE OR COUNTRY) 02000135 10004133

MATSUSHITA-ELECTRIC INDUSTRIAL CO., LTD.

Osaka, JAPAN

FC:1501

1420.00 OP

62 FC:1504

309.00 OP

Please check the appropriate assignee category or categories (will not be printed on the patent): ☐ Individual ☒ Corporation or other private group entity ☐ Government

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☐ a. Applicant claims SMALL ENTITY status. See 37 CFR 1.27.

☐ b. Applicant is no longer claiming SMALL ENTITY status. See 37 CFR 1.27(g)(2).

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Authorized Signature _____

Date November 16, 2006

Typed or printed name James E. Ledbetter

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